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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Ji Eun JANG

Confirmation No.: 6545

Appl. No.:

10/629,672

Examiner: Unassigned

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Art Unit: 2818

Title:

TEST MODE CIRCUIT OF SEMICONDUCTOR MEMORY DEVICE

CHANGE OF CORRESPONDENCE ADDRESS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please address all future correspondence in the above-identified application to:

Johnny A. Kumar, Esquire Heller Ehrman White & McAuliffe LLP 1717 Rhode Island Avenue, N.W. Washington, D.C. 20036 Tel. No. (202) 912-2000 Fax No. (202) 912-2020

Respectfully submitted,

Date:

Heller Ehrman White & McAuliffe 1717 Rhode Island Avenue, N.W. Washington, D.C. 20036

Telephone: (202) 912-2000 Facsimile: (202) 912-2020

for/ Johnny A. Kumar Reg. No. 34,649

> Attorney for Applicant Customer I.D. No. 26633